

AUG 16 2006

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Applicant : Valery M. Dubin et al. Art Unit : 2811
Serial No.: 10/668,986 Examiner : Junghwa M. Im
Filed : September 22, 2003 Conf. No.: 1755
Assignee : Intel Corporation
Title : DESIGNS AND METHODS FOR CONDUCTIVE BUMPS

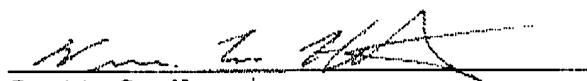
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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

A REQUEST FOR CONTINUED EXAMINATION and PETITION FOR
EXTENSION OF TIME dated August 16, 2006 are attached.

Respectfully submitted,

Date: August 16, 2006

for 
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